

10/078354

03/21/02

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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10078354	02/21/2002	438	775	2012	H Lee

**APPLICANTS: Zhang Hongyong.

JC3
2895

**CONTINUING DATA VERIFIED:

THIS APPLICATION IS A DIV OF 08/089,650 07/12/1993
WHICH IS A CON OF 07/707,092 05/29/1991 ABN

Lee

T20

** FOREIGN APPLICATIONS VERIFIED:

JAPAN 02-145069 06/01/1990

Yes

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiner's initials		<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO 740756-2440
TITLE : Method for fabricating thin-film transistor			

U.S. DEPT. OF COMM./PAT. & TM. PTO-436 (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
ISSUE FEE		Assistant Examiner	
Amount Due	Date Paid	Total Claims	Print Claim for O.G
		DRAWING	
		Sheets Drwg.	Figs. Drwg.
		Print Fig.	
		Primary Examiner	
		Application Examiner	
PREPARED FOR ISSUE			
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